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The fifth novel in Isaac Asimov's classic science-fiction masterpiece, the Foundation series THE EPIC SAGA THAT INSPIRED THE APPLE TV+ SERIES FOUNDATION Golan Trevize, former Councilman of the First Foundation, has chosen the future, and it is Gaia. A superorganism, Gaia is a holistic planet with a common consciousness so intensely united that every dewdrop, every pebble, every being, can speak for all—and feel for all. It is a realm in which privacy is not only undesirable, it is incomprehensible. But is it the right choice for the destiny of mankind? While Trevize feels it is, that is not enough. He must know. Trevize believes the answer lies at the site of humanity's roots: fabled Earth . . . if it still exists. For no one is sure where the planet of Gaia's first settlers is to be found in the immense wilderness of the Galaxy. Nor can anyone explain why no record of Earth has been preserved, no mention of it made anywhere in Gaia's vast world-memory. It is an enigma Trevize is determined to resolve, and a quest he is determined to undertake, at any cost.

Although coeducation has been the norm within private and public schools since the 1970s, single-sex education has staged a comeback in recent years as a means of addressing the academic and social problems faced by some students. Single-sex education raises controversy on ideological grounds, and in 1996 the Supreme Court struck down the all-male admissions policy at the Virginia Military Institute in a decision that has cast a legal cloud over public initiatives. In this timely book, Rosemary Salomone offers a reasoned educational and legal argument supporting single-sex education as an alternative to coeducation, particularly in the case of disadvantaged minority students. Salomone examines the history of women's education and exclusion, philosophical and psychological theories of sameness and difference, findings on educational achievement and performance, the research evidence on single-sex schooling, and the legal questions that have arisen. Correcting many of the current misconceptions about single-sex education, she argues that it is a viable option and that the road to gender equality should be paved with diverse educational opportunities for all students—regardless of race, class, or gender.

What Kids Do by Mary Engelbreit is a joyously simple celebration-through art and poignant words-of the endless little ways that children bring warmth into our hearts and light into our world. Mary Engelbreit has always had an uncanny ability to capture the quintessential moments of childhood--moments that express exhilaration, playfulness, curiosity, willfulness, high spirits, friendship, and of course, abiding love. In What Kids Do, these images are presented in a strikingly fresh format and paired with short, lively reminders of all the simple little ways kids make us smile and enrich our lives.

Electromagnetic Compatibility of Integrated Circuits: Techniques for Low Emission and Susceptibility focuses on the electromagnetic compatibility of integrated circuits. The basic concepts, theory, and an extensive historical review of integrated circuit emission and susceptibility are provided. Standardized measurement methods are detailed through various case studies. EMC models for the core, I/Os, supply network, and packaging are described with applications to conducted switching noise, signal integrity, near-field and radiated noise. Case studies from different companies and research laboratories are presented with in-depth descriptions of the ICs, test set-ups, and comparisons between measurements and simulations. Specific guidelines for achieving low emission and susceptibility derived from the experience of EMC experts are presented.

The book proposes new technologies and discusses innovative solutions to various problems in the field of communication, circuits, and systems, as reflected in high-quality papers presented at International Conference on Communication, Circuits, and Systems (IC3S 2020) held at KIIT, Bhubaneswar, India from 16 - 18 October 2020. It brings together new works from academicians,

scientists, industry professionals, scholars, and students together to exchange research outcomes and open up new horizons in the areas of signal processing, communications, and devices.

John K. Ousterhout's Definitive Introduction to Tcl/Tk-Now Fully Updated for Tcl/Tk 8.5 Tcl and the Tk Toolkit, Second Edition, is the fastest way for newcomers to master Tcl/Tk and is the most authoritative resource for experienced programmers seeking to gain from Tcl/Tk 8.5's powerful enhancements. Written by Tcl/Tk creator John K. Ousterhout and top Tcl/Tk trainer Ken Jones, this updated volume provides the same extraordinary clarity and careful organization that made the first edition the world's number one Tcl/Tk tutorial. Part I introduces Tcl/Tk through simple scripts that demonstrate its value and offer a flavor of the Tcl/Tk scripting experience. The authors then present detailed, practical guidance on every feature necessary to build effective, efficient production applications—including variables, expressions, strings, lists, dictionaries, control flow, procedures, namespaces, file and directory management, interprocess communication, error and exception handling, creating and using libraries, and more. Part II turns to the Tk extension and Tk 8.5's new themed widgets, showing how to organize sophisticated user interface elements into modern GUI applications for Tcl. Part III presents incomparable coverage of Tcl's C functions, which are used to create new commands and packages and to integrate Tcl with existing C software—thereby leveraging Tcl's simplicity while accessing C libraries or executing performance-intensive tasks. Throughout, the authors illuminate all of Tcl/Tk 8.5's newest, most powerful improvements. You'll learn how to use new Starkits and Starpacks to distribute run-time environments and applications through a single file; how to take full advantage of the new virtual file system support to treat entities such as zip archives and HTTP sites as mountable file systems; and more. From basic syntax to simple Tcl commands, user interface development to C integration, this fully updated classic covers it all. Whether you're using Tcl/Tk to automate system/network administration, streamline testing, control hardware, or even build desktop or Web applications, this is the one Tcl/Tk book you'll always turn to for answers.

"The bulk of the book is about Tcl scripting and the aspects of C programming to create Tcl extensions is given a lighter treatment."--Author.

After her nightmarish recovery from a serious car accident, Faye gets horrible news from her doctor, and it hits her hard like a rock: she can't bear children. In extreme shock, she breaks off her engagement, leaves her job and confines herself in her family home. One day, she meets her brother's best friend , and her soul makes a first step to healing.

There's only one way out. Belle Morte. One of five houses where vampires reside as celebrities and humans are paid to be their living donors. While others came here seeking fortune, I came in search of my sister who walked into Belle Morte five months ago . . . and never walked back out. Now that I'm here, the secrets about this world prove to be much bigger than I ever anticipated. And lurking around every corner are shocking insinuations regarding what happened to my sister. There's only one person who might have the answers I need, and the undeniable pull I feel toward him is terrifying: Edmond Dantès—a vampire, and my mortal enemy. The harder I try to resist him, the further I fall under his spell. And in one instant my life is irrevocably changed. My past becomes prologue and my fate becomes sealed behind these doors. Belle Morte has spoken. And it may never let me go.

Introduction to AutoCAD Plant 3D 2021 is a learn-by-doing manual focused on the basics of AutoCAD Plant 3D. The book helps you to learn the process of creating projects in AutoCAD Plant 3D rather than learning specific tools and commands. It consists of sixteen tutorials, which help you to complete a project successfully. The topics explained in the plant design process are: - Creating Projects - Creating and Editing P&IDs - Managing Data - Generating Reports - Creating 3D Structures - Adding Equipment - Creating Piping - Validate Drawings - Creating Isometric Drawings - Creating Orthographic Drawing - Project Management, and - Printing and Publishing Drawings

For the Vampire community, the Solstice Choosing has been the holiest night of the year - for a hundred thousand years. But this year, something new is about to happen. The oldest prophecies

are about to be fulfilled - and the Festival of Blessings is finally upon us.

For Electrical Engineering courses in analog layout or professional layout designers. This text covers the issues involved in successfully laying out analog integrated circuits. Hastings provides clear guidance and does not stress theoretical physics or mathematical analysis of layouts. He emphasizes cross-sections of devices and carrier-based models of device operation as compared to the more common geometric and schematic representation of devices.

Taryn Clark thought she'd outgrown the need to find her birth mother. She thought that a successful career and a comfortable life in the city were enough to be happy. Did she really need to know about the woman who had given her away? Adopted at birth, her first few years were happy. It hadn't mattered that she didn't know her heritage; she had parents who loved her and wanted her. But divorce, and then death, ripped their tiny family apart, and at the tender age of six, she entered the foster care system. Over the next dozen years, she shuffled from home to home. Finding her roots seemed an impossible dream. But dreams are resilient. An unexpected discovery awakens old yearnings of belonging to a family, of being part of something bigger than herself. Finding the brief, ambiguous note from her birth mother is enough to unfurl the ribbons of hope still binding her heart. Her quest takes her to Lancaster County, Pennsylvania and the heart of the Plain community. Aided by her unique eye color, a healthy dose of luck, and the private investigator she hires, Taryn finds her birth family easily enough, but finding the truth is another matter. In all her musings, she never imagined a scenario where her mother might be Amish. She never imagined that the fabric of her life might be a patchwork of faith and fear, stitched together with a dark family secret. Taryn is determined to trace her roots, even if it means digging in the mud to do so. Now she's caught in the quicksand of a shocking discovery and the consequences of choices made, almost forty years ago. She'll risk everything to uncover the truth and to claim the family--and the roots--she so desperately craves.

Heroes are hard to find, especially for Sean, who, at fifteen years old, has never had it easy. He's estranged from his father and abused by his mother—Sean feels he has nowhere to turn. Suspended from school and with a mounting police record, he is one step away from serious trouble. Sentenced to community service for violating curfew, Sean is sent to Mr. Hassler's farm, where he's forced to confront his fears and take charge of his own life. When Sean is faced with a dangerous situation that tests his true character, he learns just what it means to be a hero. In an age where it seems that few truly worthy heroes exist, S. L. Rottman has written a compelling and realistic portrayal of the conflicts that drive a young man toward his destiny.

Five great poets of the T'ang dynasty (eighth and ninth centuries A.D.) are represented in this collection: Wang Wei, Li Po, Tu Fu, Li Ho, and Li Shang-Yin. Each poet is introduced by the translator and represented by a selection that spans the poet's development and career. These constitute some of the greatest lyric poems ever written.

The X-Men are back in the cinema. Wolverine, Professor X, Cyclops, Jean Grey and the rest of the team return in X2, facing a new threat so dangerous that former enemy Magneto must join their ranks to defeat it.

Get the most out of the new features in CSS3 Cascading Style Sheets (CSS3) boasts many new features that designers love. Things like better mobility, cleaner code, less maintenance, and basic interactivity without JavaScript are just a few. Get thoroughly up to speed on CSS3 with CSS3 For Dummies. Whether you're a web developer or designer with a lot or very little experience in HTML and CSS, or just want assistance applying it to multiple browser and mobile apps, this book delivers. Explore in depth how CSS3 effects and transforms work and how to use them for animation and interactivity. Explains CSS3 in detail for web developers and designers who may or may not know HTML, CSS, or tools like Dreamweaver Instructs how to define and apply CSS3 effects and transforms that work in multiple browsers Covers how to animate CSS3 effects and transforms with CSS and basic JavaScript Provides detailed information and helpful examples, in the friendly, non-in-

timidating, For Dummies style Start putting the great new features of CSS3 to work on your next project with CSS3 For Dummies.

Annotation The HTML5 canvas is revolutionizing graphics and visualizations on the Web. Powered by JavaScript, the HTML5 Canvas API enables web developers to create visualizations and animations right in the browser without Flash. Although the HTML5 Canvas is quickly becoming the standard for online graphics and interactivity, many developers fail to exercise all of the features that this powerful technology has to offer. The HTML5 Canvas Cookbook begins by covering the basics of the HTML5 Canvas API and then progresses by providing advanced techniques for handling features not directly supported by the API such as animation and canvas interactivity. It winds up by providing detailed templates for a few of the most common HTML5 canvas applications: data visualization, game development, and 3D modeling. It will acquaint you with interesting topics such as fractals, animation, physics, color models, and matrix mathematics. By the end of this book, you will have a solid understanding of the HTML5 Canvas API and a toolbox of techniques for creating any type of HTML5 Canvas application, limited only by the extent of your imagination.

This book explains integrated circuit design for manufacturability (DfM) at the product level (packaging, applications) and applies engineering DfM principles to the latest standards of product development at 22 nm technology nodes. It is a valuable guide for layout designers, packaging engineers and quality engineers, covering DfM development from 1D to 4D, involving IC design flow set-up, best practices, links to manufacturing and product definition, for process technologies down to 22 nm node, and product families including memories, logic, system-on-chip and system-in-package.

Design, Automation, and Test for Low-Power and Reliable Flexible Electronics provides an in-depth overview of flexible electronics from their applications, manufacturing processes, device characteristics, to circuit and system design solutions. With rapid advances in flexible semiconducting materials, the performance of TFT circuits has been improving significantly and concerns about their ambient stability have been alleviated to a great extent over the past few years. After a brief introduction to flexible electronics, the book highlights its key difference from silicon electronics, and the challenges and opportunities of circuit design for emerging applications such as wearable electronics, personalized healthcare, and flexible displays. While the main focus of the book is on the design, EDA and test issues, it also offers brief technical reviews on TFT technologies, manufacturing methods, and flexible photovoltaics for the purpose of providing a more comprehensive introduction of this emerging field.

This book describes a collection of extensions, tools, and applications that have played an essential role in the success of the Tcl scripting language and the Tk toolkit. Both packages are suited to a wide range of tasks, from serving as an embedded control language to controlling NASA's most advanced spacecraft.

Today's computers must perform with increasing reliability, which in turn depends on the problem of determining whether a circuit has been manufactured properly or behaves correctly. However, the greater circuit density of VLSI circuits and systems has made testing more difficult and costly. This book notes that one solution is to develop faster and more efficient algorithms to generate test patterns or use design techniques to enhance testability - that is, "design for testability." Design for testability techniques offer one approach toward alleviating this situation by adding enough extra circuitry to a circuit or chip to reduce the complexity of testing. Because the cost of hardware is decreasing as the cost of testing rises, there is now a growing interest in these techniques for VLSI circuits. The first half of the book focuses on the problem of testing: test generation, fault simulation, and complexity of testing. The second half takes up the problem of design for testability: design techniques to minimize test application and/or test generation cost, scan design for sequential logic circuits, compact testing, built-in testing, and various design techniques for testable systems. Hideo Fujiwara is an associate professor in the Department of Electronics and Communication, Meiji University. Logic Testing and Design for Testability is included in the Computer Systems Series, edited by Herb Schwetman.

Ariana had no reason to think this battle would be any different than the rest. Slay the beasts, save the day, another mission accomplished for the Queen. But when another Huntress is hurt, Ariana is obligated to use her power to heal, allowing a Hunter to find her. Now, her mission to bring the Oracle into the tribe is in serious jeopardy unless she can shake her Hunter. Lance isn't letting Ariana out of his sight. He's prepared his whole life to go to battle alongside his mate, and now that he's found her, his only goal is to convince her that falling in love with him is the only option. But Ariana must decide where her loyalty ultimately lies - with her Hunter or her Huntresses. And the cost of

her choice just might be death for one of them... Each story in The Order of the Wolf Series is standalone story that can be enjoyed in any order. Series Order: Book #1: Cursed plus Wolf Slayer Book #2: Wolves' Bane Book #3: Spell Weaver plus Mayhem Book #4: Valiant Heart Book #5: Beast Rising

Introduction to Mechatronic Design is ideal for upper level and graduate Mechatronics courses in Electrical, Computing, or Mechanical & Aerospace Engineering. Unlike other texts on mechatronics that focus on derivations and calculations, Introduction to Mechatronics, 1e, takes a narrative approach, emphasizing the importance of building intuition and understanding before diving into the math. The authors believe that integration is the core of mechatronics and students must have a command of each of the domains to create the balance necessary for successful mechatronic design and devote sections of the book to each area, including mechanical, electrical, and software disciplines, as well as a section on system design and engineering. A robust package of teaching and learning resources accompanies the book.

Tyson's journey from student to senior executive when an entirely new world of human communications came into being. He traces the development of corporate identity, vision, and activities of Bell-Northern Research (BNR), which would become one of the most innovative and widely respected research-and-development organizations in the world.

This book describes in detail all required technologies and methodologies needed to create a comprehensive, functional design verification strategy and environment to tackle the toughest job of guaranteeing first-pass working silicon. The author first outlines all of the verification sub-fields at a high level, with just enough depth to allow an engineer to grasp the field before delving into its detail. He then describes in detail industry standard technologies such as UVM (Universal Verification Methodology), SVA (SystemVerilog Assertions), SFC (SystemVerilog Functional Coverage), CDV (Coverage Driven Verification), Low Power Verification (Unified Power Format UPF), AMS (Analog Mixed Signal) verification, Virtual Platform TLM2.0/ESL (Electronic System Level) methodology, Static Formal Verification, Logic Equivalency Check (LEC), Hardware Acceleration, Hardware Emulation, Hardware/Software Co-verification, Power Performance Area (PPA) analysis on a virtual platform, Reuse Methodology from Algorithm/ESL to RTL, and other overall methodologies.

The theme for the 2019 conference is Novel Computing Architectures. Papers will include discussions on the advent of Artificial Intelligence and the promise of quantum computing that are driving disruptive computing architectures; Neuromorphic chip designs on one hand, and Quantum Bits on the other, still in R&D, will introduce new computing circuitry and memory elements, novel materials, and different test methodologies. These novel computing architectures will require further innovation which is best achieved through a collaborative Failure Analysis community composed of chip manufacturers, tool vendors, and universities.

Electronic design automation (EDA) is among the crown jewels of electrical engineering. Without EDA tools, today's complex integrated circuits (ICs) would be impossible. Doesn't such an important field deserve a comprehensive, in-depth, and authoritative reference? The Electronic Design Automation for Integrated Circuits Handbook is that reference, ranging from system design through physical implementation. Organized for convenient access, this handbook is available as a set of two carefully focused books dedicated to the front- and back-end aspects of EDA, respectively. What's included in the Handbook? EDA for IC System Design, Verification, and Testing This first installment examines logical design, focusing on system-level and micro-architectural design, verification, and testing. It begins with a general overview followed by application-specific tools and methods, specification and modeling languages, high-level synthesis approaches, power estimation methods, simulation techniques, and testing procedures. EDA for IC Implementation, Circuit Design, and Process Technology Devoted to physical design, this second book analyzes the classical RTL to GDS II design flow, analog and mixed-signal design, physical verification, analysis and extraction, and technology computer aided design (TCAD). It explores power analysis and optimization, equivalence checking, placement and routing, design closure, design for manufacturability, process simulation, and device modeling. Comprising the work of expert contributors guided by leaders in the field, the Electronic Design Automation for Integrated Circuits Handbook provides a foundation of knowledge based on fundamental concepts and current industrial applications. It is an ideal resource for designers and users of EDA tools as well as a detailed introduction for newcomers to the field.

This book constitutes the refereed proceedings of the 23rd International Symposium on VLSI Design and Test, VDAT 2019, held in Indore, India, in July 2019. The 63 full papers were carefully re-

viewed and selected from 199 submissions. The papers are organized in topical sections named: analog and mixed signal design; computing architecture and security; hardware design and optimization; low power VLSI and memory design; device modelling; and hardware implementation.

This third collection of Black Lace sexy short stories features a variety of British and American contributors. It presents a collection of women's erotic fiction that combines strong characters with a sting in the tail.

Proceedings of SPIE present the original research papers presented at SPIE conferences and other high-quality conferences in the broad-ranging fields of optics and photonics. These books provide prompt access to the latest innovations in research and technology in their respective fields. Proceedings of SPIE are among the most cited references in patent literature.

In the past decade, substrate noise has had a constant and significant impact on the design of analog and mixed-signal integrated circuits. Only recently, with advances in chip miniaturization and innovative circuit design, has substrate noise begun to plague fully digital circuits as well. To combat the effects of substrate noise, heavily over-designed structures are generally adopted, thus seriously limiting the advantages of innovative technologies. Substrate Noise: Analysis and Optimization for IC Design addresses the main problems posed by substrate noise from both an IC and a CAD designer perspective. The effects of substrate noise on performance in digital, analog, and mixed-signal circuits are presented, along with the mechanisms underlying noise generation, injection, and transport. Popular solutions to the substrate noise problem and the trade-offs often debated by designers are extensively discussed. Non-traditional approaches as well as semi-automated techniques to combat substrate noise are also addressed. Substrate Noise: Analysis and Optimization for IC Design will be of interest to researchers and professionals interested in signal integrity, as well as to mixed signal and RF designers.

The Tcl language and Tk graphical toolkit are simple and powerful building blocks for custom applications. The Tcl/Tk combination is increasingly popular because it lets you produce sophisticated graphical interfaces with a few easy commands, develop and change scripts quickly, and conveniently tie together existing utilities or programming libraries. One of the attractive features of Tcl/Tk is the wide variety of commands, many offering a wealth of options. Most of the things you'd like to do have been anticipated by the language's creator, John Ousterhout, or one of the developers of Tcl/Tk's many powerful extensions. Thus, you'll find that a command or option probably exists to provide just what you need. And that's why it's valuable to have a quick reference that briefly describes every command and option in the core Tcl/Tk distribution as well as the most popular extensions. Keep this book on your desk as you write scripts, and you'll be able to find almost instantly the particular option you need. Most chapters consist of alphabetical listings. Since Tk and mega-widget packages break down commands by widget, the chapters on these topics are organized by widget along with a section of core commands where appropriate. Contents include: Core Tcl and Tk commands and Tk widgets C interface (prototypes) Expect [incr Tcl] and [incr Tk] Tix TclX BLT Oratcl, SybTcl, and Tclodbc

1001 Walks You Must Experience Before You Die features wide-ranging, carefully chosen routes varying from the rugged delights of the Pembrokeshire Coastal Path to the wilderness of Jamaica, and the Harz Witches' Trail high up in the German mountains. The hand-picked excursions take in mountain passes, woodland paths, ancient Native-American trails, and much more. There are easy walks for beginners - some lasting barely an hour - and more demanding challenges that may take several weeks to complete. Every fact-packed entry provides a wealth of information about a must-try walk, including essential details about its start and finish points, overall distance, difficulty rating, maps, and the time it should take to complete. In short, 1001 Walks You Must Experience Before You Die is an essential reference book and guide for all those who love to get out of their cars, get off their bikes, and lace up their walking shoes.

Because of the continuous evolution of integrated circuit manufacturing (ICM) and design for manufacturability (DfM), most books on the subject are obsolete before they even go to press. That's why the field requires a reference that takes the focus off of numbers and concentrates more on larger economic concepts than on technical details. Semiconductors: Integrated Circuit Design for Manufacturability covers the gradual evolution of integrated circuit design (ICD) as a basis to propose strategies for improving return-on-investment (ROI) for ICD in manufacturing. Where most books put the spotlight on detailed engineering enhancements and their implications for device functionality, in contrast, this one offers, among other things, crucial, valuable historical background and roadmapping, all illustrated with examples. Presents actual test cases that illustrate product challenges, examine possible solution strategies, and demonstrate how to select and im-

plement the right one This book shows that DfM is a powerful generic engineering concept with potential extending beyond its usual application in automated layout enhancements centered on proximity correction and pattern density. This material explores the concept of ICD for production by breaking down its major steps: product definition, design, layout, and manufacturing. Averting

extended discussion of technology, techniques, or specific device dimensions, the author also avoids the clumsy chapter architecture that can hinder other books on this subject. The result is an extremely functional, systematic presentation that simplifies existing approaches to DfM, outlining a clear set of criteria to help readers assess reliability, functionality, and yield. With careful consid-

eration of the economic and technical trade-offs involved in ICD for manufacturing, this reference addresses techniques for physical, electrical, and logical design, keeping coverage fresh and concise for the designers, manufacturers, and researchers defining product architecture and research programs.